
**Summary**

The **ADM-PCIE-KU3** is a high performance reconfigurable Half-Length, low profile x16 PCIe form factor board based on the Xilinx™ Kintex UltraScale range of Platform FPGAs.

The ADM-PCIE-KU3 features two independent channels of DDR3 memory capable of 1600MT/s (fitted with two 8GB SODIMMs), high speed I/O, SATA connections, Dual QSFP ports supporting 10G Ethernet, voltage/temperature/current control and monitoring, passive air-cooled heat sink.

**Features**
**Applications:**

- Data Processing
- System Modelling
- Market Analysis

**Target Devices:**

Xilinx Kintex UltraScale: XCKU060, XCKU075 (FFVA1156)

**Memory:**

**ECC-SODIMM** - Dual 204-pin SODIMM Sockets for memory speeds up to 1600MT/s: Fitted with two 8GB SODIMMs at the factory.

**FLASH** - 1GBit of BPI x 16 configuration flash

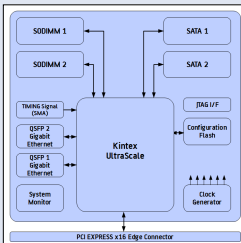
**Front Connector I/O:**

Dual QSFP cages for high speed optical communication including 10 and 40 Gigabit Ethernet.

Timing Signal Input (SMA)

**Other I/O:**

Dual 6pin SATA Interfaces





## Specification

Product Name	ADM-PCIE-KU3
Target Devices	Xilinx Kintex UltraSCALE - XCKU060, XCKU075 (FFVA1156)
Host I/F	PCI Express® Gen3 x16
Interface	PCI Express® Gen3 x16
Memory	<b>ECC-SODIMM</b> - Dual 204-pin SODIMM Sockets for memory speeds up to 1600MT/s: Fitted with two 8GB SODIMMs at the factory. <b>FLASH</b> - 1GBit of BPI x 16 configuration flash
Front I/O	Dual QSFP cages for high speed optical communication including 10 and 40 Gigabit Ethernet. Timing Signal Input (SMA)
Other I/O	Dual 6pin SATA Interfaces
Special Functions	Dual QSFP cages for high speed optical communication including 10 and 40 Gigabit Ethernet Dual SATA Interfaces Timing Signal Input on front panel (SMA)
Clocks	All Clocks derived from a local 100MHz LVDS oscillator with 20ppm 2x 100MHz - PCI Express® Clocks and user design 2x 400MHz - Memory Interfaces and user design 1x 200MHz - IODELAY reference 1x 156.25MHz - 10GbE SFP 1x 150MHz - SATA Interface 1x 100MHz - User Design 1x 250MHz - User design 1x 133MHz - FPGA Configuration Clock
Device Configuration	FPGA Configured via the on board Configuration Flash Flash programmed via JTAG
Software	Comprehensive Software Development Kit with source code for example software and FPGA designs.
Environmental	<b>Temperature:</b> Operating Temperature 0° to +55°C <b>EMC:</b> FCC 47CFR Part 2 EN55022 Equipment Class B

## Ordering Codes

## ADM-PCIE-KU3(x)-2

Kintex Ultrascale-7 Device

x

KU060, KU075

Address: 4 West Silvermills Lane,  
Edinburgh, EH3 5BD, UK  
Telephone: +44 131 558 2600  
Fax: +44 131 558 2700  
email: sales@alpha-data.com  
website: <http://www.alpha-data.com>

Address: 3507 Ringsby Court Suite 105,  
Denver, CO 80216  
Telephone: (303) 954 8768  
Fax: (866) 820 9956 toll free  
email: sales@alpha-data.com  
website: <http://www.alpha-data.com>