

SBC6511

Rugged 6U VPX Single Board Computer with 9th Gen Intel Xeon E CPU and Aligned to SOSA™ Standard

The SBC6511 Rugged Single Board Computer (SBC) from Abaco Systems features the new high performance, highly integrated Intel® Xeon® E processor ('Coffee Lake Refresh').

High Performance, High Reliability

The new Xeon E combines six 9th Generation Core™ i7 technology processing cores with a rich I/O mix, all with the backing of Intel's Embedded Use Conditions – ideal for long term, high reliability applications.

The SBC6511 offers memory resources including 64 GB of high speed DDR4 SDRAM and up to 256 GB NAND Flash (NVMe SSD), plus a range of I/O including DisplayPort™, USB, GPIO and serial comms. Two on-board mezzanine expansion sites are also provided for enhanced system flexibility.

In alignment with the SOSA™ technical standards and in support of the DOD's C4ISR/EW Modular Open Suite of Standards (CMOSS), the SBC6511's data plane fabric connectivity is via 40G capable Ethernet fat pipes, with a Gen 3 capable PCle™ quad fat pipe providing the expansion plane. Control plane connectivity on

the backplane is via two 10G capable Ethernet ultra-thin pipes and two 1000BASE-T thin pipes for external connection.

Available in a range of air- and conduction cooled build levels with extended temperature capability, the SBC6511 is designed to meet the requirements of a wide range of applications from industrial through to fully rugged defense and aerospace programs.

It is especially adept at applications requiring heavy throughput and extensive I/O processing, like radar and SIGINT applications.

Enhanced Security Features

The SBC6511 incorporates a range of security features designed to assist with user defined Anti-Tamper and Information Assurance strategies. These include an inherently secure FPGA solution (Xilinx® Zynq® UltraScale+™), and support for Intel's Trusted Execution Technology. The FPGA can be utilized to instantiate a range of Abacodefined security features, or by the customer to embed application specific features.

FEATURES:

- · Xilinx Zyng Ultrascale+
- 6U OpenVPX single board computer
- 9th Generation Intel® Core™ i7
- · 12 MB shared cache
- Soldered DDR4 SDRAM with FCC
- Up to 256 GB solid state disk drive
- Multiple Data Plane Fabric Configurations (10/40GE)
- Multiple PCle® Expansion Plane Fabric configurations
- · Both rear & front I/O ports
- On-board expansion sites
- Trusted Platform Monitor (TPM)
- Elapsed Time Indicator (ETI)
- Rich software choice, including Deployed Test Software, plus operating systems support for Microsoft® Windows, Open Linux® and VxWorks®
- Middleware software support of AXIS Pro Suite and Health Toolkit



SBC6511 Rugged 6U VPX Single Board Computer with 9th Gen Intel Xeon E CPU and Aligned to SOSA Standard

Specifications

Processor

- Xeon E CPU (E-2276ME) formerly known as Coffee Lake Refresh
- 6-cores at 2.8 GHz
- 45W TDP
- CM246 PCH (Platform Controller Hub)
- 12 MB Cache

Maximum memory configuration of 64 GB DDR4 SDRAM @ 2400 MHz with ECC

Onboard NVMe Solid State Drive (SSD)

· Up to 256 GB (64 GB standard)

OpenVPX Module Profiles

MOD6-PAY-4F1Q1H4U1T1S1S1TU2U2T 1H-12.6.3-2

USB Ports

2x USB 3.2 Gen 1 ports routed to P4

Expansion Plane

x16 PCle, 2 x8 PCle, or 4 x4 PCle from a Gen3 capable switch to P2

Data Plane

2x 10/40 Gb KX4/KR4 Ethernet

Control Plane

- 2x 10/100/1000BASE-T
- 2x 1000BASE-KX/10GBASE-KR

Management Plane

IPMI (Baseboard Management Controller) in accordance with VITA 46.11

- 2x RS-232 or 1x 422 routed to P4
- 2x RS-232/LVCMOS routed to P1/P5

4x SATA 3.0 capable (6 Gb/s), routed to P4

Video Controller

1x DisplayPort port routed to P4

Front I/O (air-cooled only)

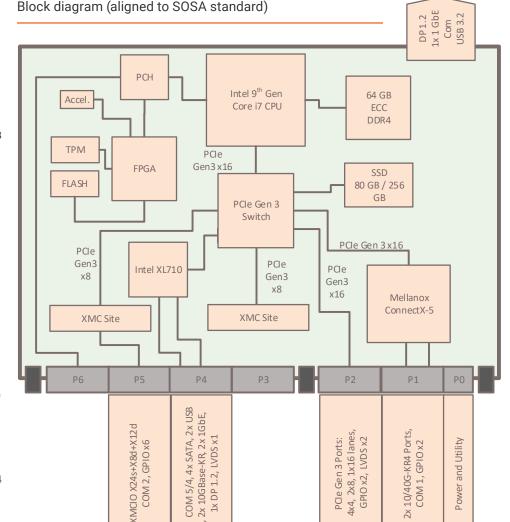
- 1x USB 3.2 Gen 1 port
- 1x 10/100/1000BASE-T
- · 1x RS-232/422
- 1x DisplayPort

XMC Expansion Slots

- Both sites (one routed to P5)
- XMC x8 PCle Gen3
- Available with VITA 42 connectors (contact factory for VITA 61 connectors)

General Purpose I/O

- Up to 10x GPIO, 5V tolerant, each capable of generating an interrupt; routed to P2/P4
- 3x GPIO (LVDS) routed to P1/P2/P5



FPGA

- Xilinx Zyng UltraScale+ FPGA (ZU7EG) with advanced security features
 - Enhanced anti-tamper features
 - Encryption
 - Physically unclonable functions (PUF)
 - Zeroization

SOSA

Allows for alignment with OpenVPX payload profile: MOD6-PAY-4F1Q1H4U1T1S1S1TU2U21H-12.6.3-6

Other Hardware Features

- Hardware Write Protection
- On-board three axis accelerometer

LED

- 1x power
- · 3x status LEDs and 4x BIT status (software control)

NVRAM / Watchdog / ETI / TPM

1 MB non-volatile RAM (FRAM)

4x4,

- Watchdog timer (software programmable)
- Elapsed Time Indicator
- **Temperature Sensors**
- TPM 2.0 (Trusted Platform Module)

Power Requirements

+12V and 3.3V_AUX



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Onboard NVMe Solid State Drive (SSD)

Up to 256 GB (64 GB standard)

OpenVPX Module Profiles

- MOD6-PAY-4F1Q2U2T-12.2.1-8
- MOD6-PAY-4F1Q2U2T-12.2.1-15

6x USB 3.2 Gen 1 ports routed to P4/P6

Expansion Plane

x16 PCle, 2 x8 PCle, or 4 x4 PCle from a Gen3 capable switch to P2

Data Plane

2x 10/40 Gb KX4/KR4 Ethernet

Control Plane

- 2x 10/100/1000BASE-T
- 2x 1000BASE-KX/10GBASE-KR

Management Plane

IPMI (Baseboard Management Controller) in accordance with VITA 46.11

- 2x RS-232 or 1x 422 routed to P4
- 2x RS-232/LVCMOS routed to P1/P5

4x SATA 3.0 capable (6 Gb/s), routed to P4

Video Controller

- 1x DisplayPort port routed to P4
- 1x DisplayPort port routed to P6

Front I/O (air-cooled only)

- 1x USB 3.2 Gen 1 port
- 1x 10/100/1000BASE-T
- 1x RS-232/422
- 1x DisplayPort

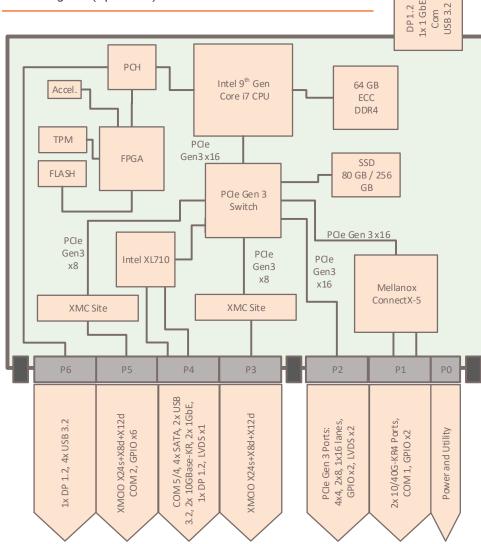
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