

XRM2-ADC-S4/3G

31st March 2019

Datasheet Revision: 1.0





Board Features





 External Clock Input . Thermal monitoring of the ADCs



The XRM2-ADC-S4/3G is an XRM2 I/O Module, providing one Analog to Digital converter with 8-bit resolution and sampling rates up to 3Gsps.

Aimed at IF/Baseband Signal Sampling, the sampling clock can be sourced from either an external clock source or from a clock generated within the attached FPGA board. An Auxiliary I/O port is provided for use as a trigger control or for general purpose signaling. An additional two ports are

available for use as high-speed interconnect between boards for synchronisation. The built-in thermal monitor allows the user to check the operating temperature of the ADC. Provided as part of the sample design is the functionality to read the temperature of the device, and software to monitor this and recalibrate the ADC if the thermal drift is sufficient. The software will also shut the ADC down if the device starts to go over the maximum operating temperature

Deliverables

XRM2-ADC-S4/3G Board One Year Warranty One Year Technical Support Input/Output Interfaces 1x Single Analog to Digital Converter

Resolution: 8-bit Max Sample Freq: 3Gsps Bandwidth: 30MHz to 1700MHz nominal Impedance: 500

Connector: SMA

connector

1x External clock input Impedance: 500 (AC coupled) Levels: 424 mV peak to peak (-3.5dBm) nominal 283 mV peak to peak (-7dBm) minimum up to 1.414 V peak to peak (+7dBm) maximum at

Connector: SMA 1x External clock output

Impedance: 500 (AC coupled) Levels: ±400mV nominal Connector: SMA

1x Auxiliary I/O

Impedance: input 4k7Ω (DC coupled) Levels: +3V3 LVTTL Connector: SMA

2x Synchronisation I/O

Levels: 2V5 Logic (DC coupled) Connector: U.FL - Limited Access in assembled system





Support

Example UCF, HDL files and Application software are provided with the board.

Environmental Specification

Temperature Ranges

Cooling	Operating Temperatures		Storage Temperature	
Option	Min	Max	Min	Max
AC0	0°C	55°C	-40°C	85°0
Operating Hur	width: Units 95	% (non-condone	(na)	

EMC Standards FCC 47CFR Part 2

EN55022:2010 Equipment ClassB

Order Code: XRM2-ADC-S4/3G(h)

Option	Code	Description of Options
Heatsink		blank = No Heastink, /HTSK-XRM-ADC-HS-1 = Heatsink Fitted

Address: 160 Dundee Street, Suite 4A. Edinburgh, EH11 1DQ, UK Telephone: +44 131 558 2600
Fax: +44 131 558 2700
email: sales@alpha-data.com
website: http://www.alpha-data.com

Address: 611 Corporate Circle Suite H Golden, CO 80401 | Telephone: (303) 954 8768 | Fax: (866) 820 9956 - toll free email: sales@alpha-data.com website: http://www.alpha-data.com