

XRM2-COAXPRESS

31st March 2019 Datasheet Revision: 1.3



- · Multiple CoaXpress Interfaces
- Video Frame Grabber Image Processing
- Video Communications

Board Features · CoaXpress Interface Circuitry

. Power over CoaXpress circuitry

The XRM2-COAXPRESS is an XRM2 I/O Module which provides Coaxpress capability for Alpha

It provides the user with the ability to implement computationally-intensive applications such as frame grabbers, digital video communications and image processing systems in the FPGA when using a compatible camera. Provided in two configurations: an 8 Channel Receiver or as a 6 channel receiver/2 channel transmitter.

Deliverables

XRM2-COAXPRESS Board One Year Warranty One Year Technical Support

Board Format

Alpha Data XRM2 I/O Module

Innut/Output Interfaces

6/8x Coaxpress Receive channels - /R8 = 8

Channels 2/0x Coaxpress Transmit channels - /R8 = 0

Channels

1x PoCXP power in (+24V)





Support

Example UCF, HDL files and Application software are provided with the board.

Environmental Specification

Temperature Ranges

Cooling Option	Operating Temperatures		Storage Temperatures	
	Min	Max	Min	Max
AC1	-40°C	70°C	-55°C	100°C

EMC Standards

FCC 47CFR Part 2

EN55022:2010 Equipment ClassB

	Ordering Information
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	Order Code: XRM2-COAXPRESS(IO)(t)

Description of Options
/R8 = Eight CoaXPress Receive Channels, /R6T2 = Six CoaXPress Receive Channels and 2 CoaXPress Transmit Channels

10 blank = DACs not fitted . DAC Interface /C1 = DACs are fitted (only for /R6T2 option)

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